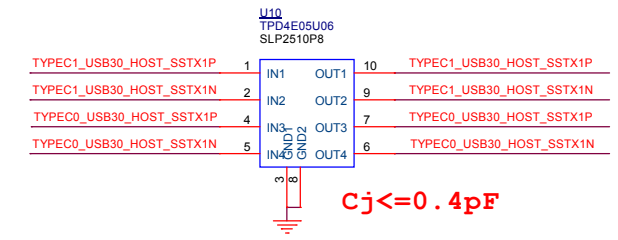
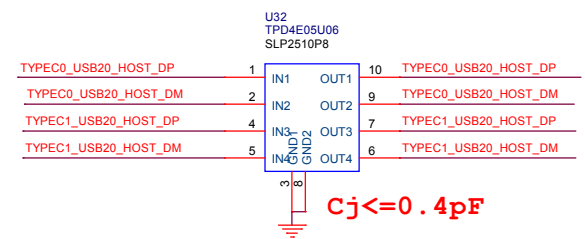
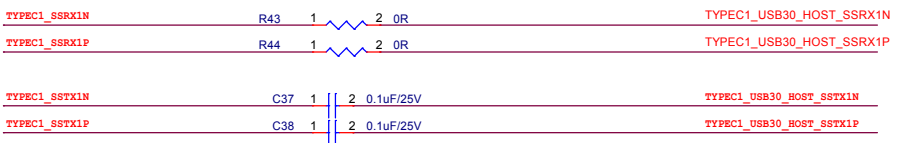
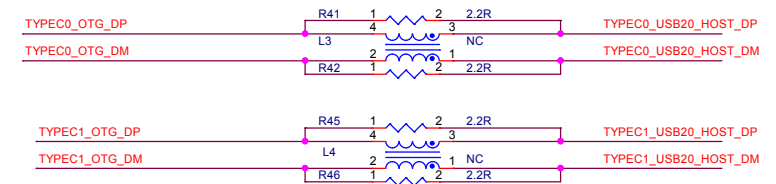
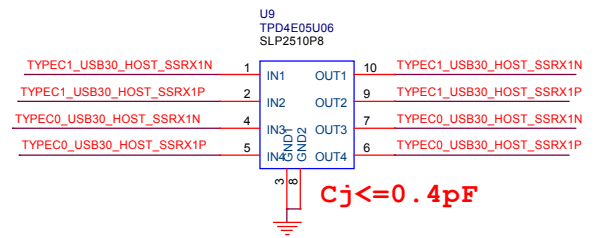
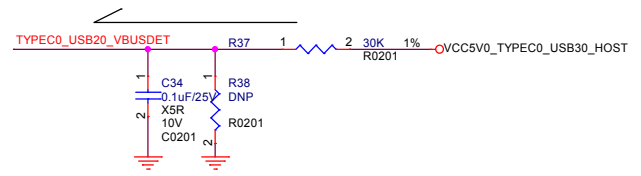
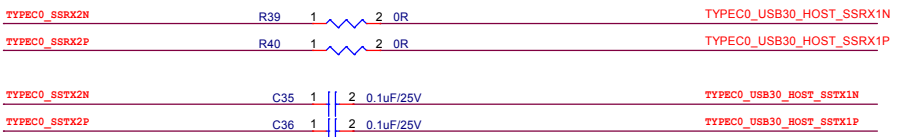
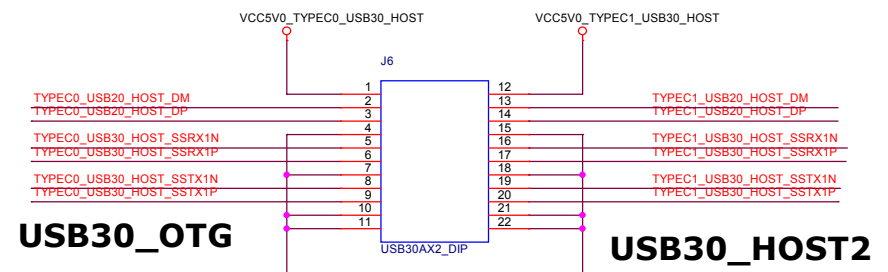
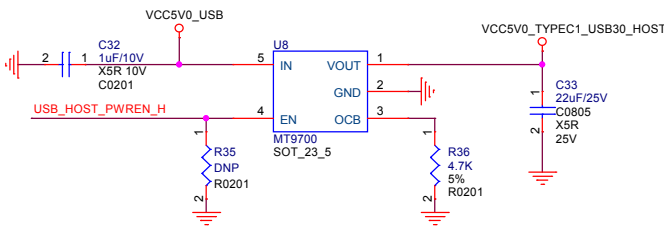
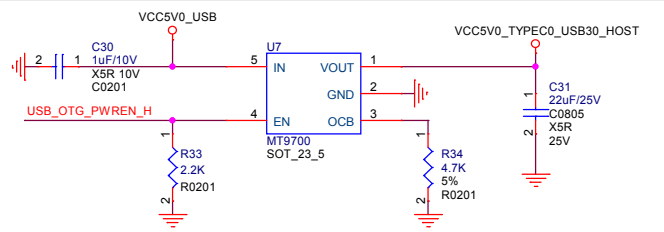
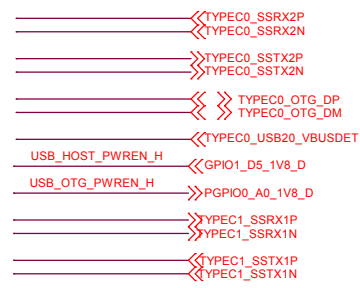
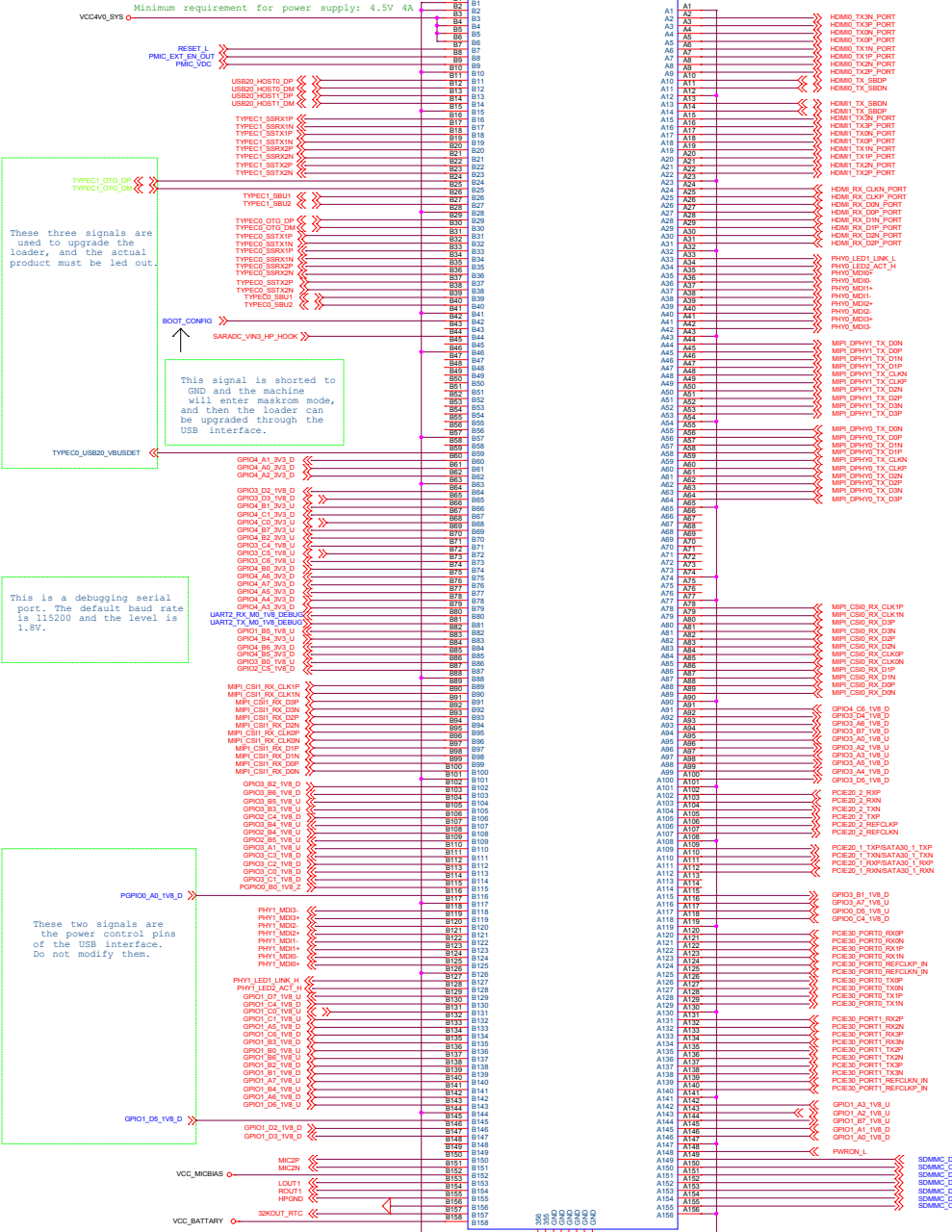


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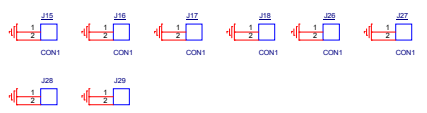
These three signals are used to upgrade the loader, and the actual product must be led out.

This signal is shorted to GND and the machine will enter maskrom mode, and then the loader can be upgraded through the USB interface.

This is a debugging serial port. The default baud rate is 115200 and the level is 1.8V.

These two signals are the power control pins of the USB interface. Do not modify them.

The SDMMC interface connects the TF card as an interface for system boot.



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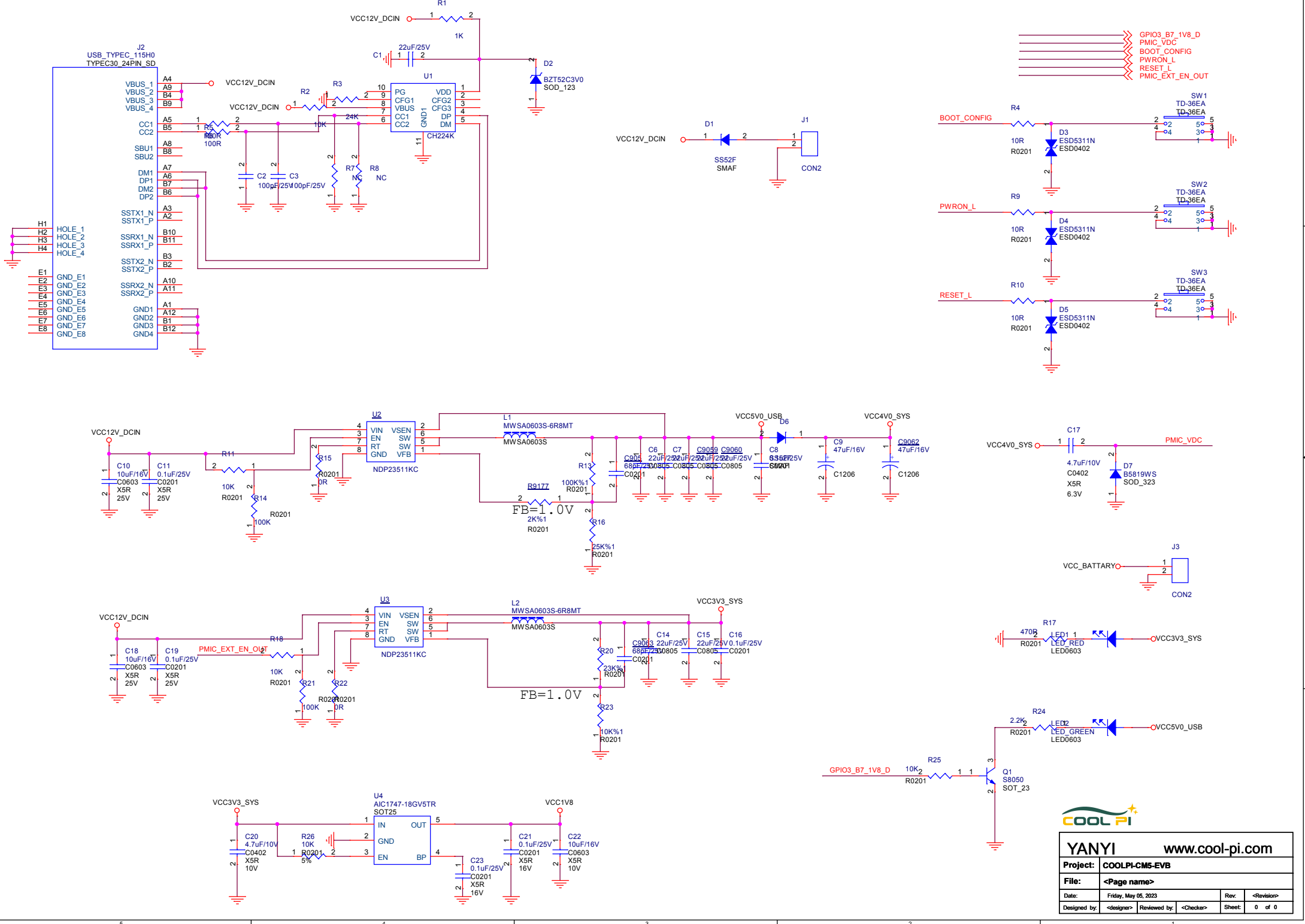
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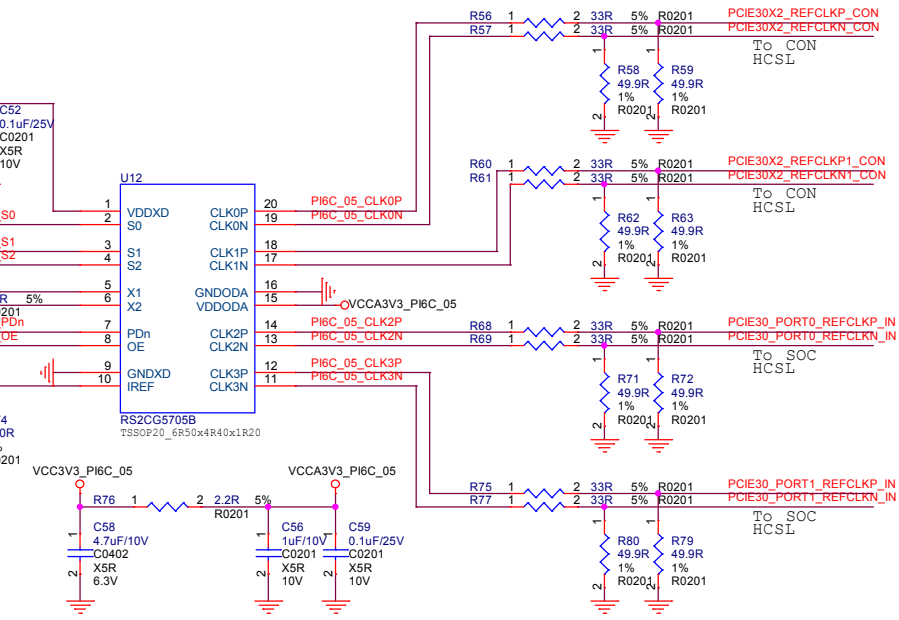
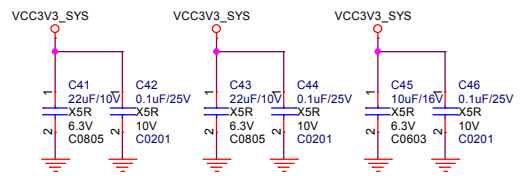
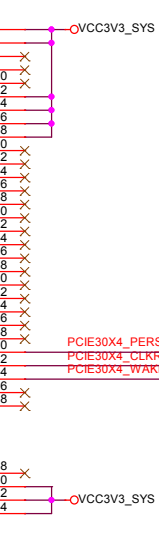
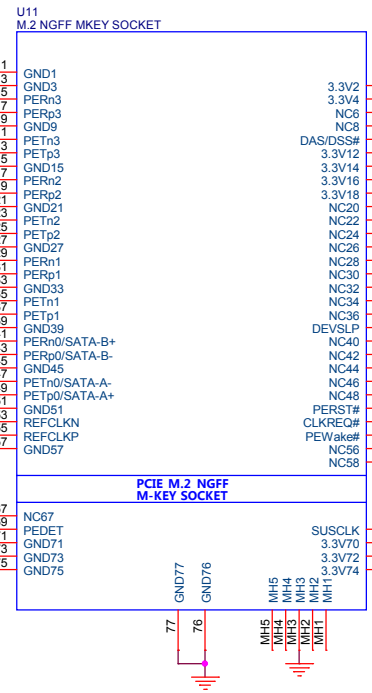
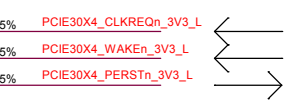
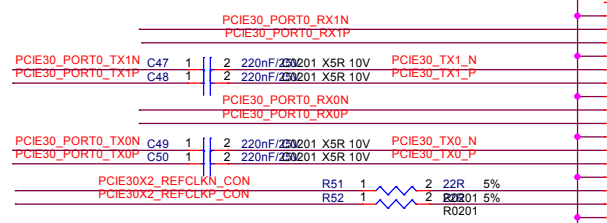
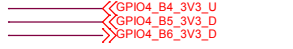
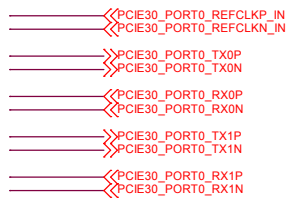
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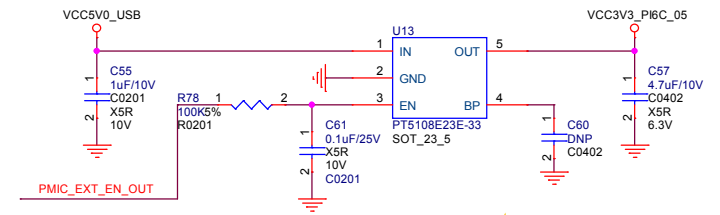
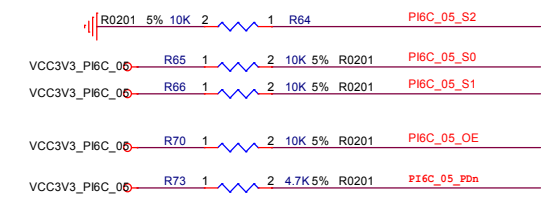
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PI6C_S2	PI6C_S1	PI6C_S0	Spread %	Out Freq
0	0	0	-0.5	100MHz
0	0	1	-1.0	100MHz
0	1	0	-1.5	100MHz
0	1	1	No Spread	100MHz

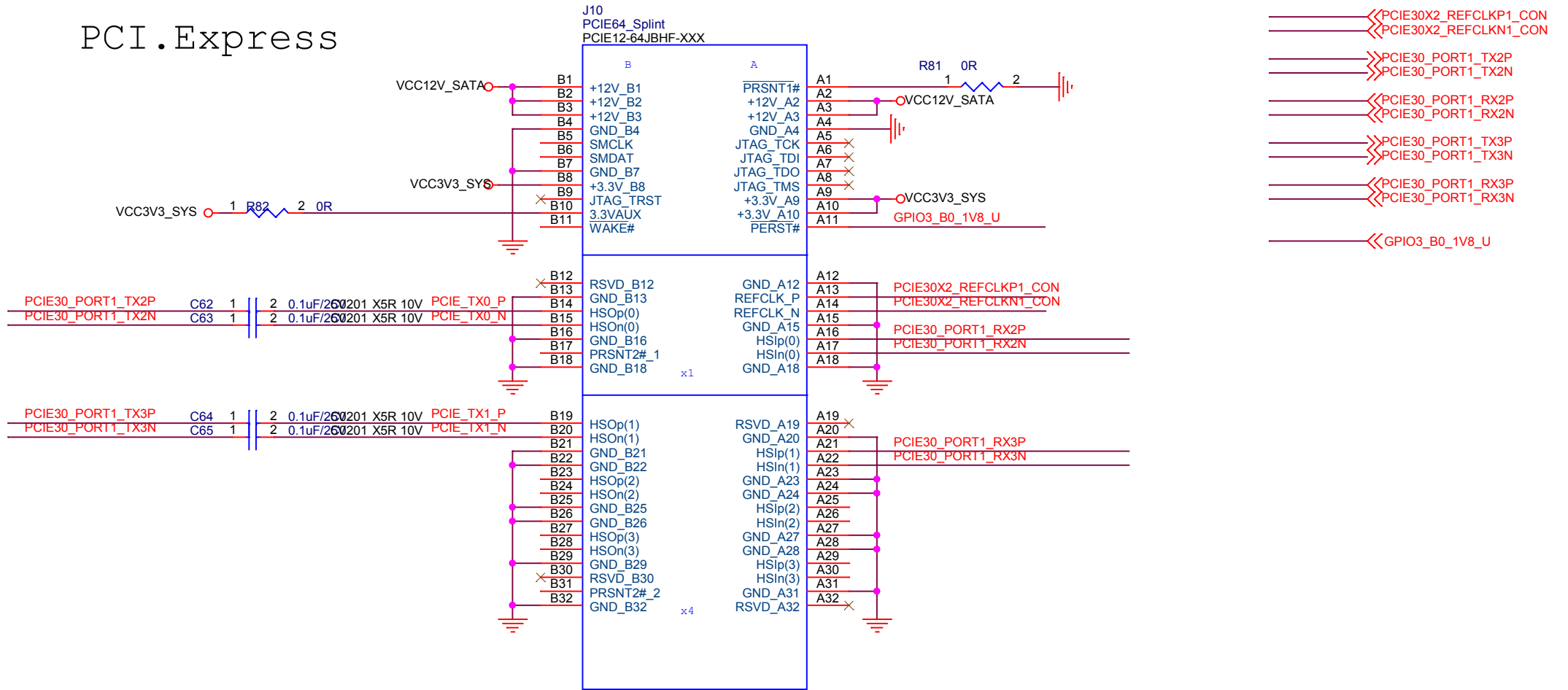


If board target trace impedance is 50ohm then R = 475ohm providing an IREF of 2.32 mA . The output current (IOH) is 6 * IREF . 6x2.32x50=696mV

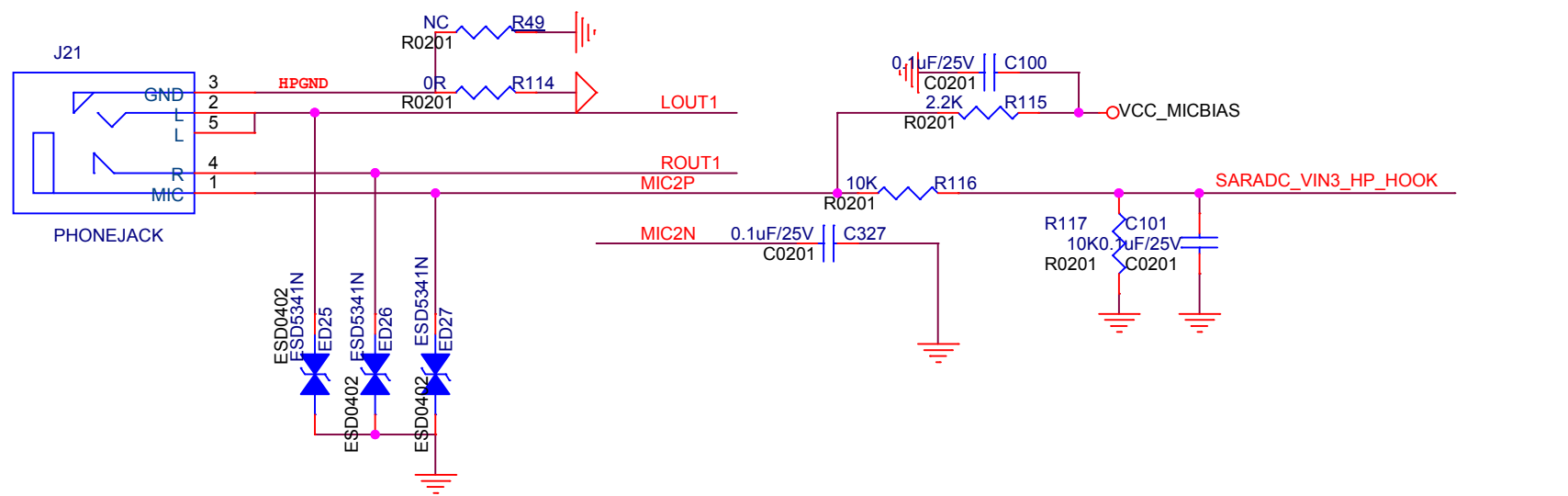
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PCI.Express



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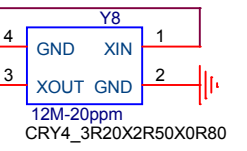
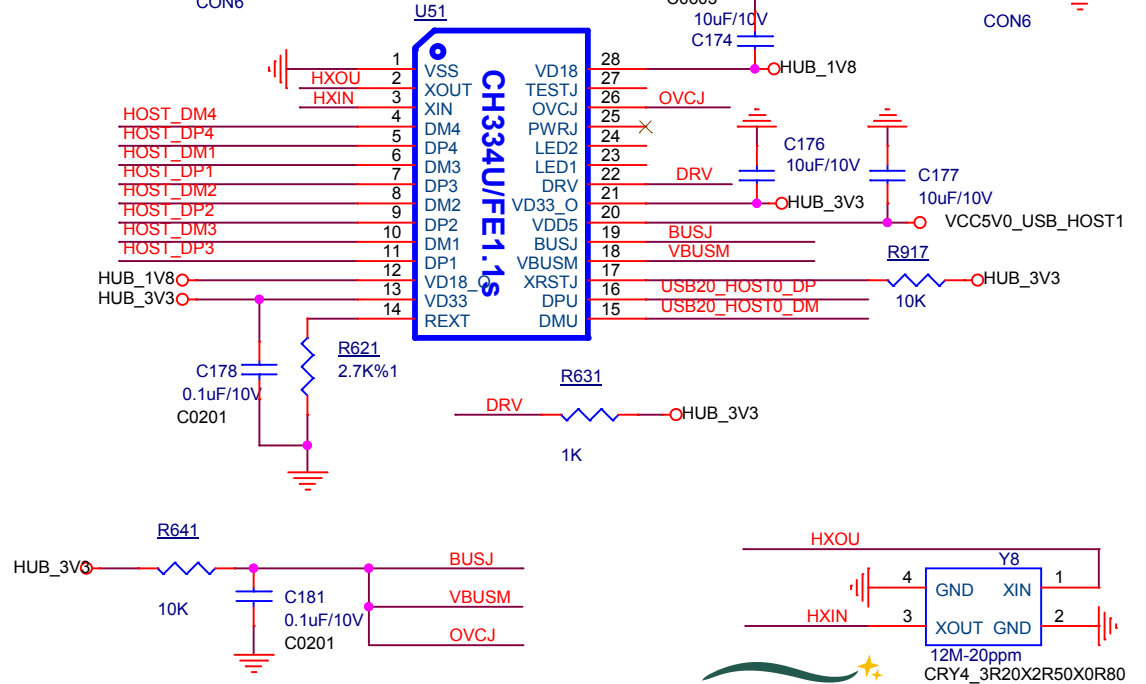
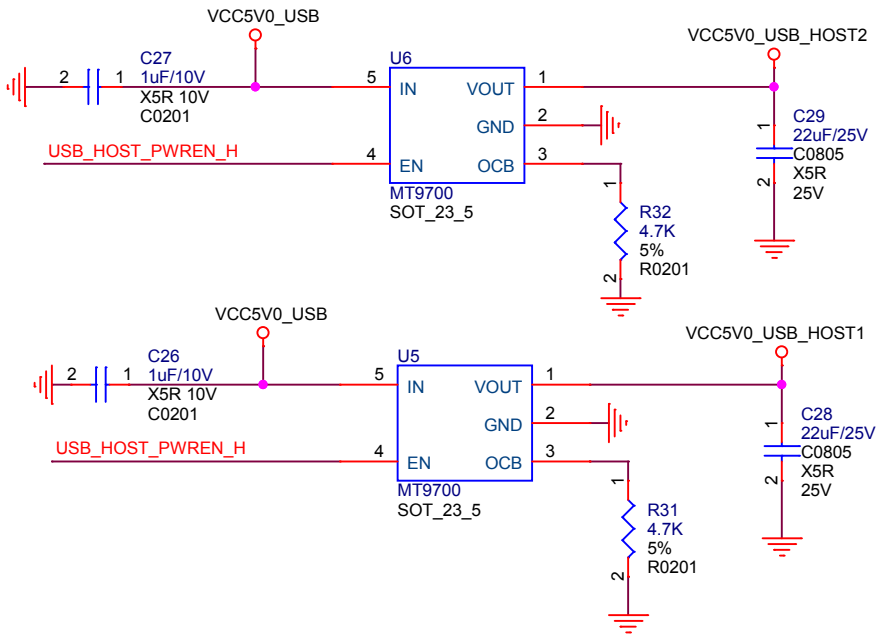
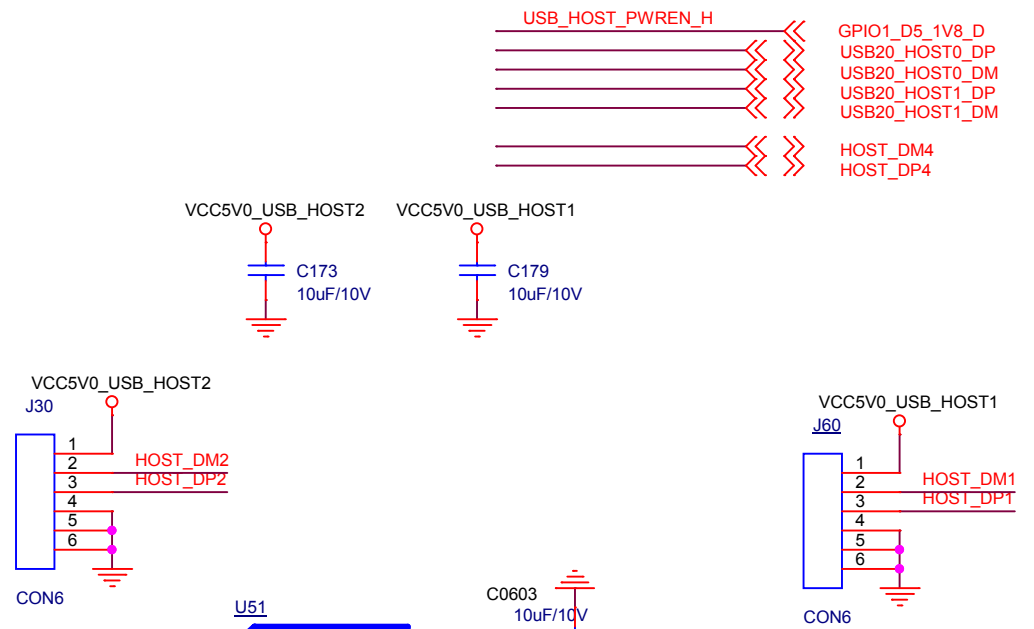
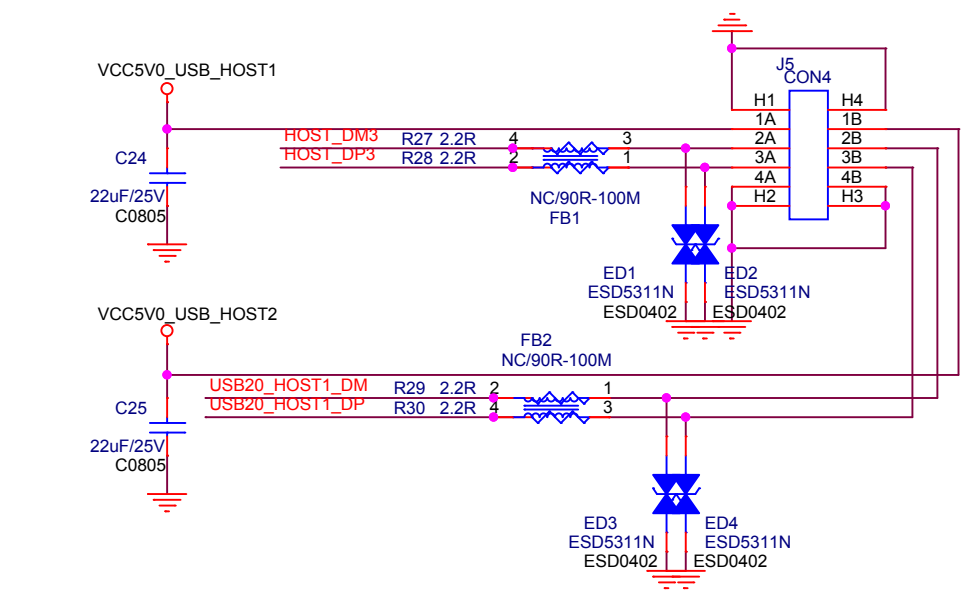


MIC2P
 MIC2N
 LOUT1
 ROUT1
 HPGND

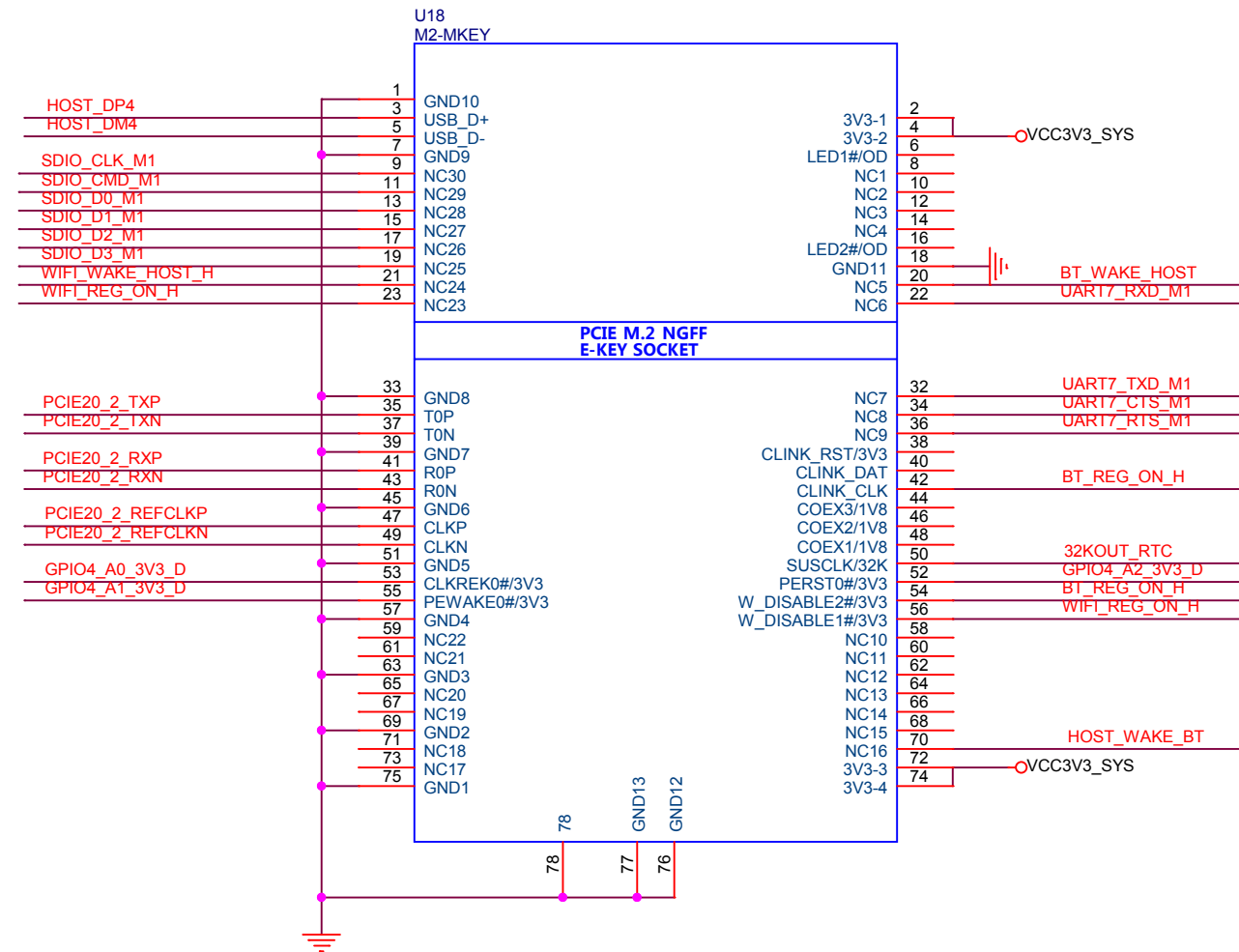
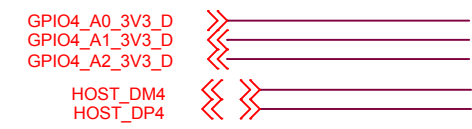
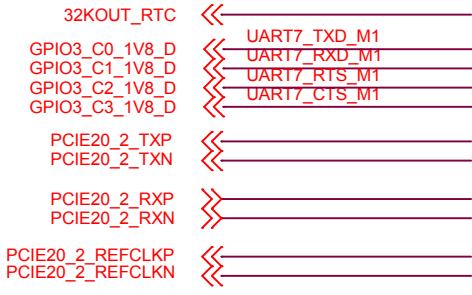
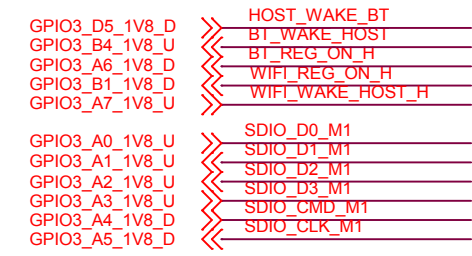
SARADC_VIN3_HP_HOOK



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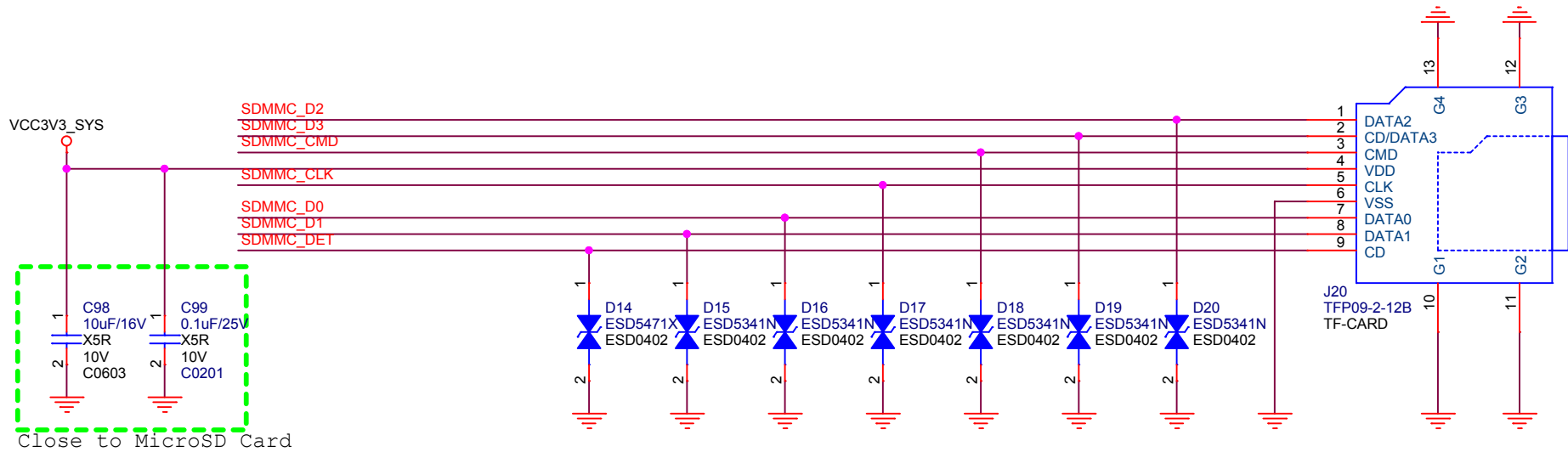


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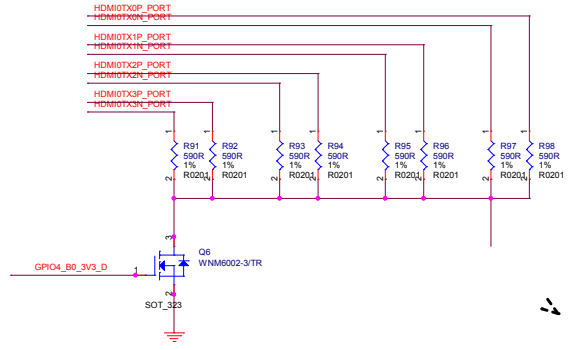
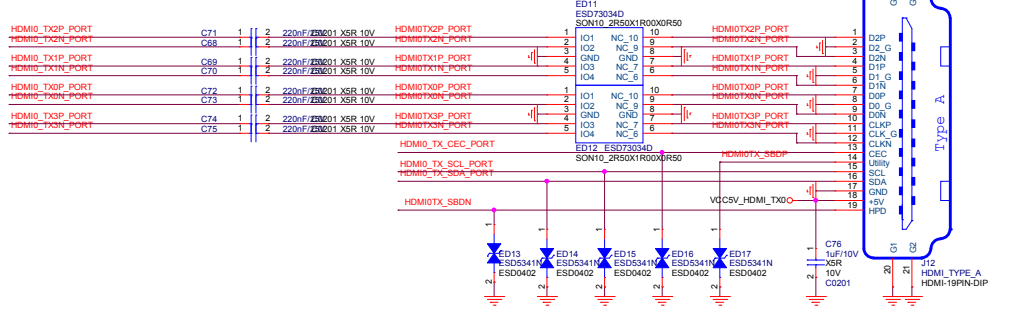
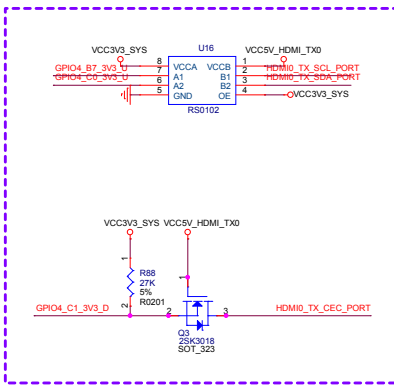
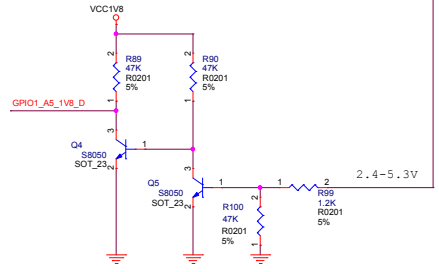
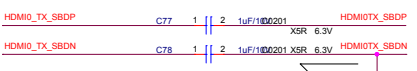
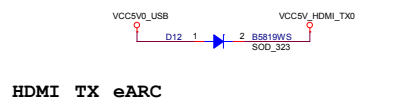
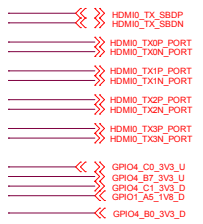
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SDMMC_D0
 SDMMC_D1
 SDMMC_D2
 SDMMC_D3
 SDMMC_CMD
 SDMMC_CLK
 SDMMC_DET



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HDMI TX0

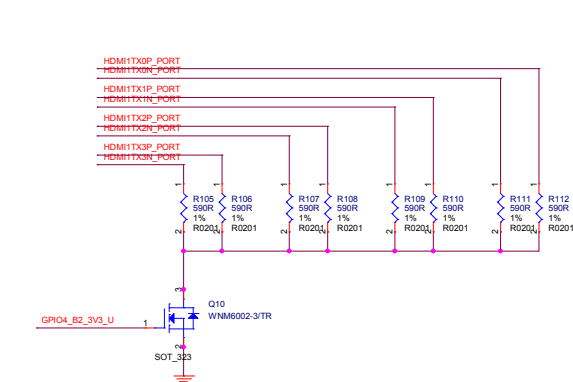
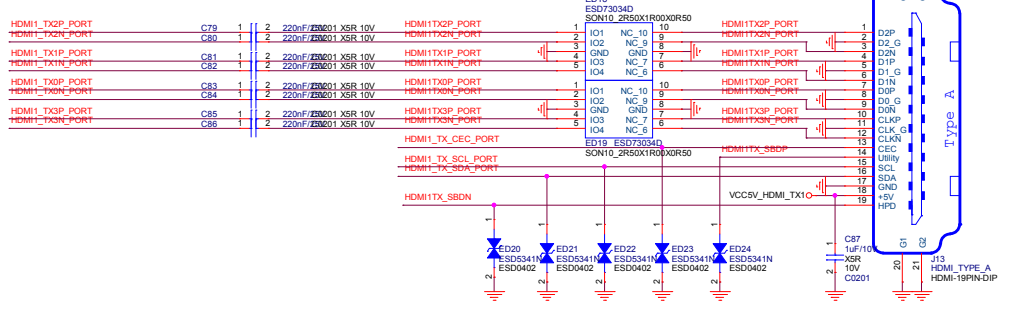
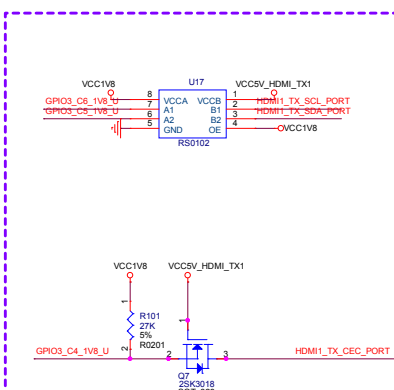
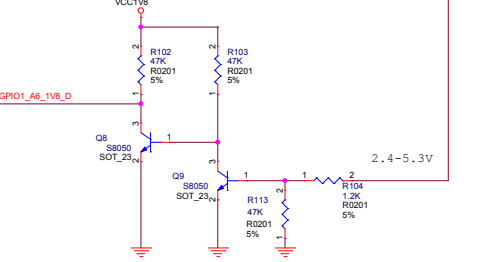
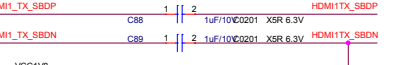
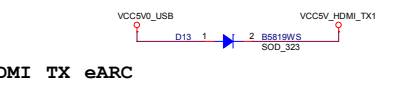
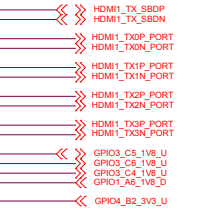


Note:
 HDMI2.0: Stuff R5040, R5041, R5042
 No stuff Q5004, Q5005, Q5006
 HDMI2.1: Stuff Q5004, Q5005, Q5006
 No stuff R5040, R5041, R5042

Note:
 The controller only support AC coupled link in order to backward compatibility or to meet HDMI2.0 (1.4b) DC common mode spec and Voff, need do R based level-shift.
 Switch on in HDMI2.0 (TMDS) mode
 Switch off in HDMI2.1 (FRL) mode

Rds=1.7ohm/2.6V
 Coss=7.33pF

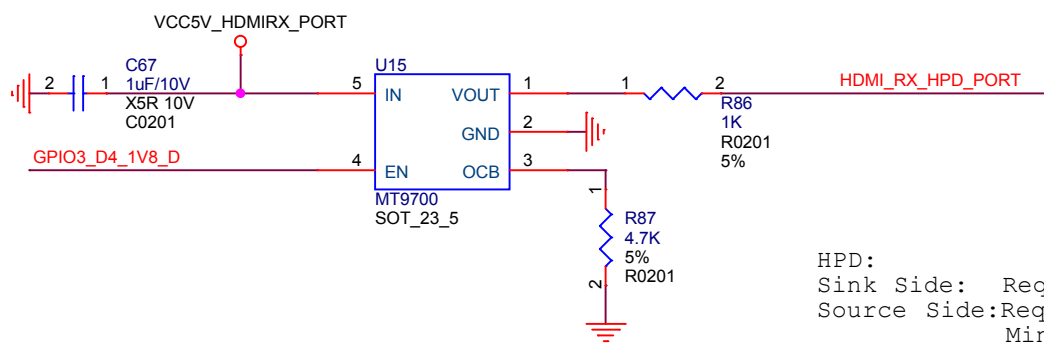
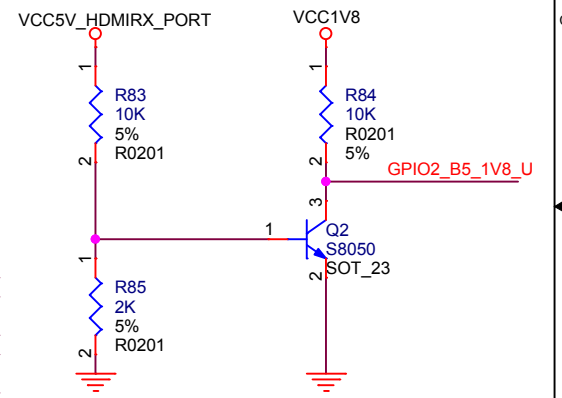
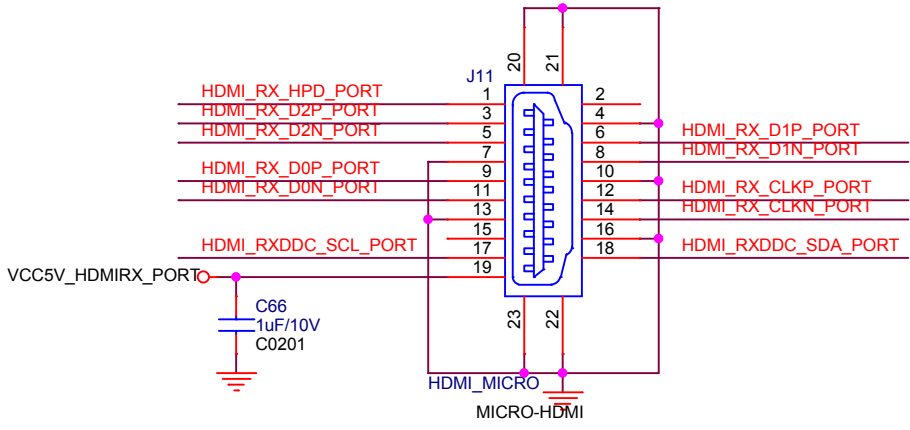
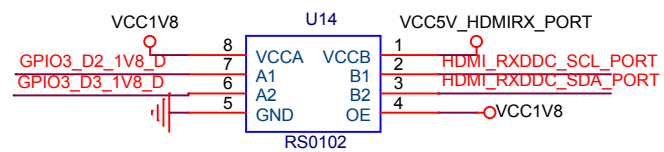
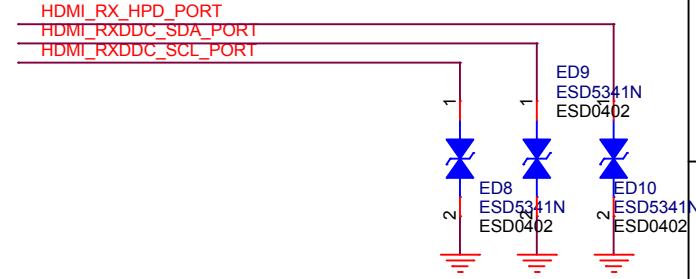
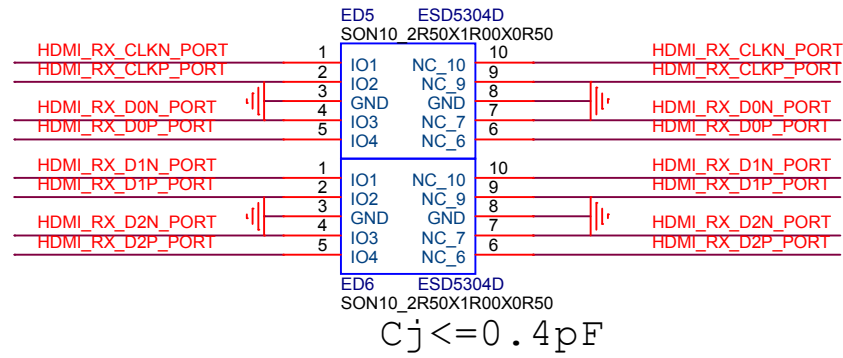
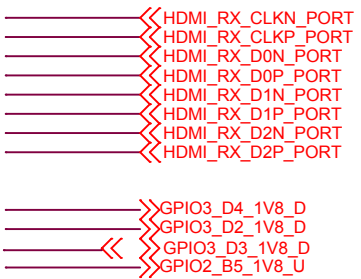
HDMI TX1



Note:
 HDMI2.0: Stuff R5043, R5044, R5045
 No stuff Q5010, Q5011, Q5012
 HDMI2.1: Stuff Q5010, Q5011, Q5012
 No stuff R5043, R5044, R5045

HDMI 2.0 RX (OPTION1)

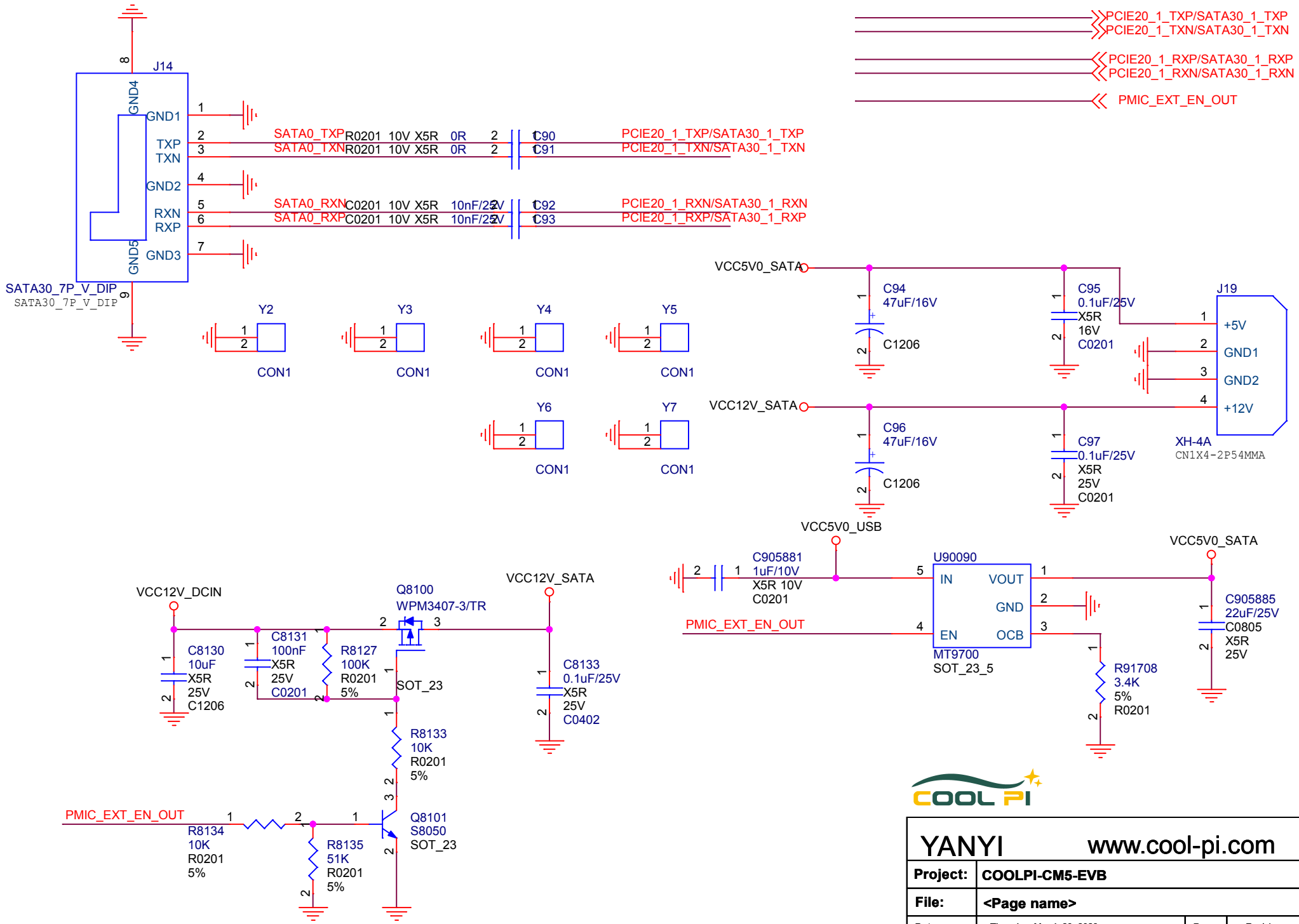
Note:
 (This HDMI2.0 RX interface has low probability anomalies, such as splash screen and restart, in scenarios such as frequent plug and pull, high and low resolution switching, etc. The probability is between 0.1% and 1%. We are still trying to debug and solve these problems. If the user will frequently use various peripherals to insert this HDMI2.0 RX interface, it is recommended to use external bridges connecting chips to improve compatibility to avoid affecting product production.)



HPD:
 Sink Side: Require output, Min 2.4V; Max 5.3V
 Source Side: Require input and Detection.
 Min 2.0V; Max: 5.3V

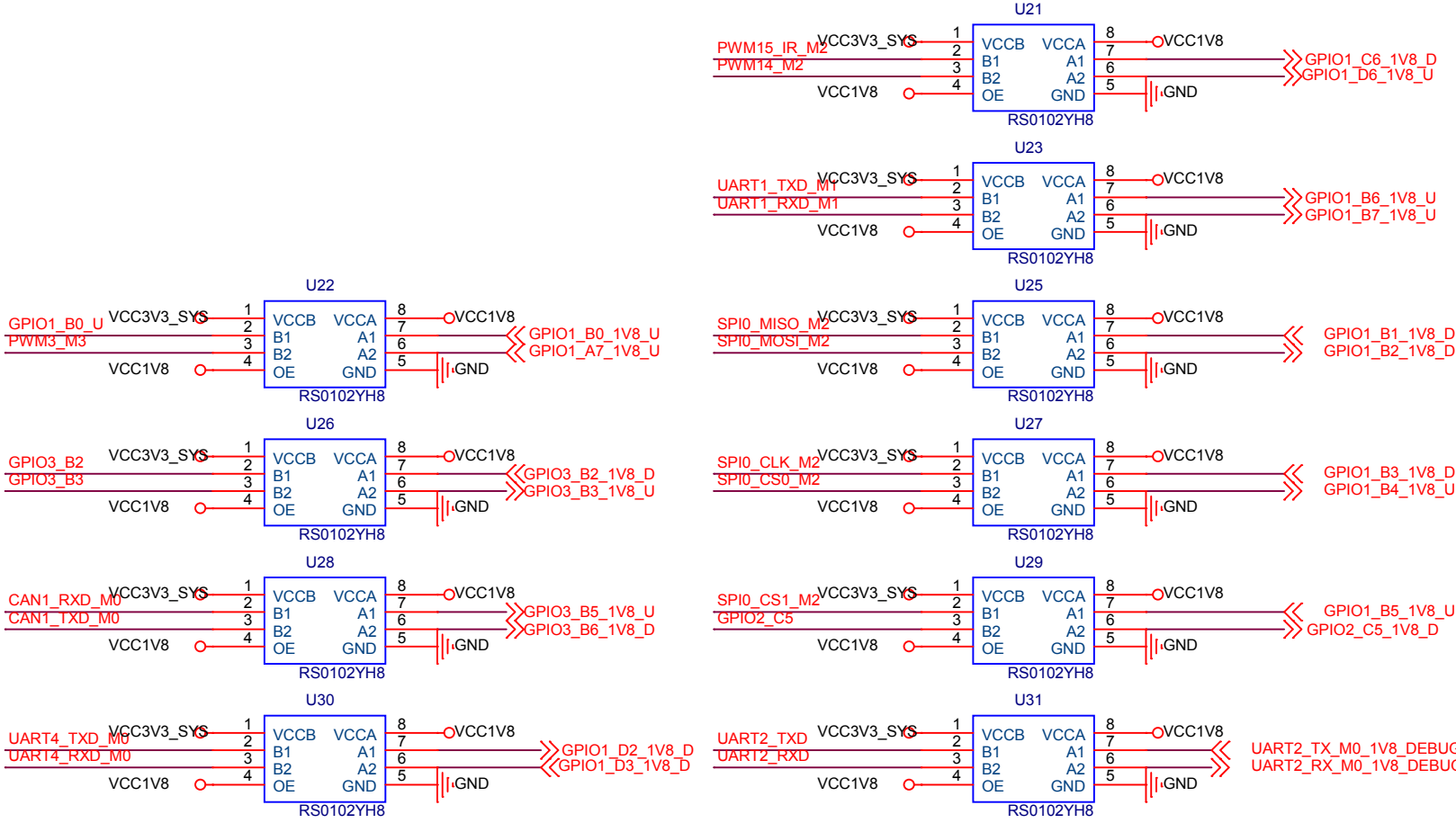
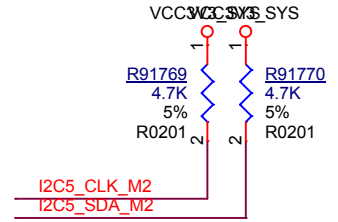
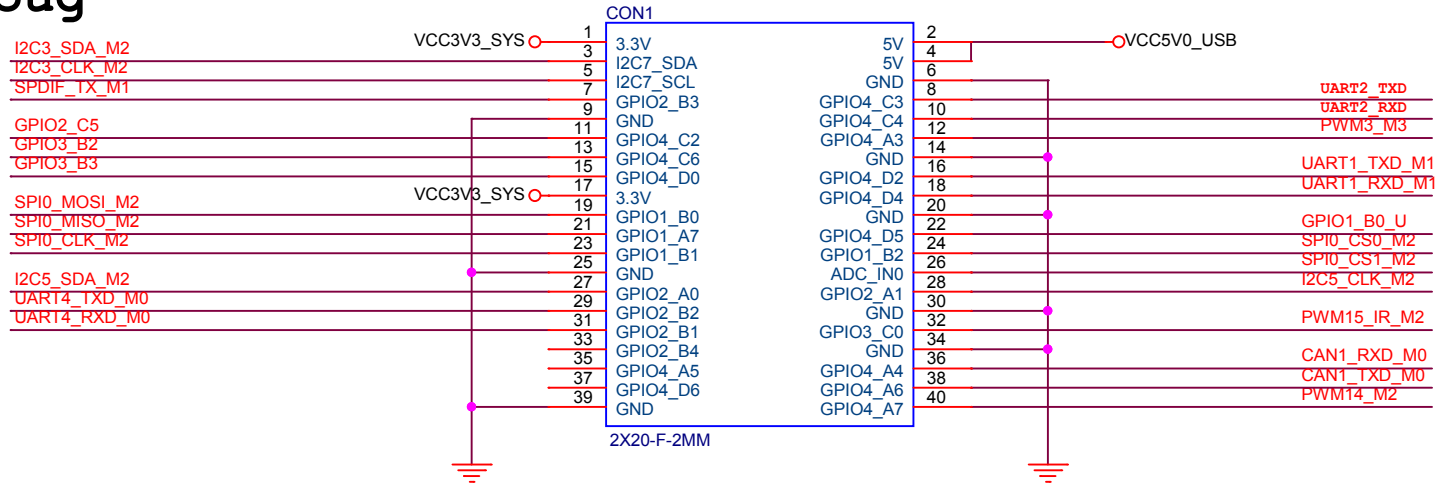


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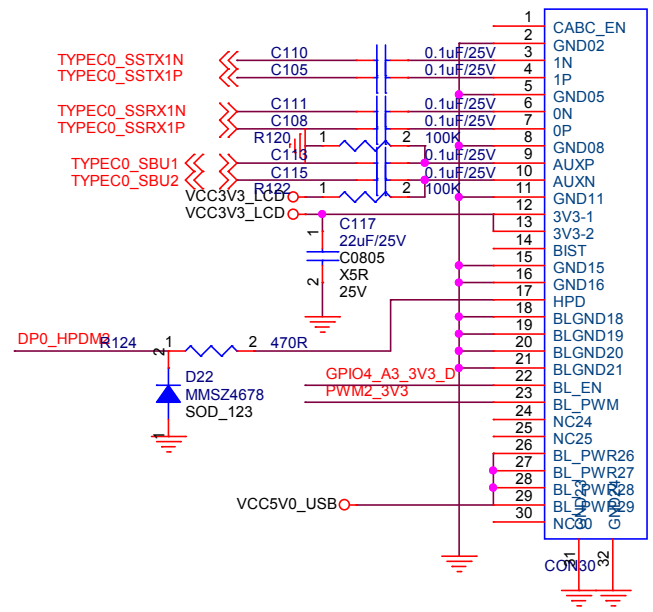
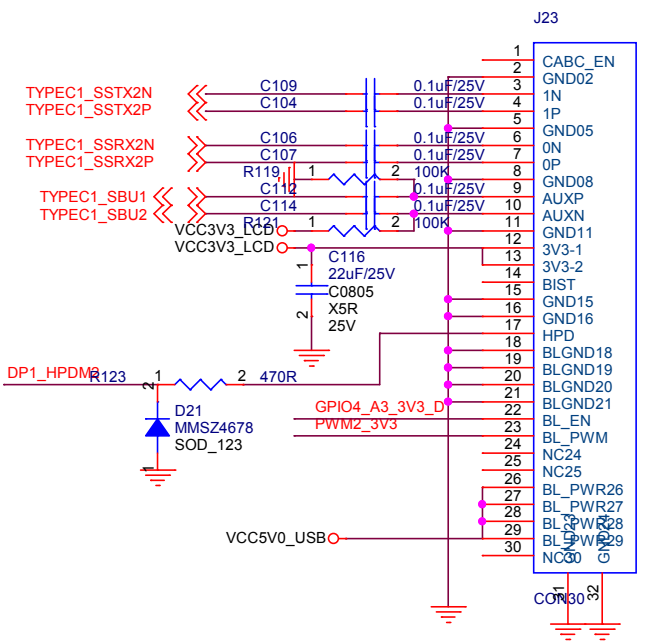
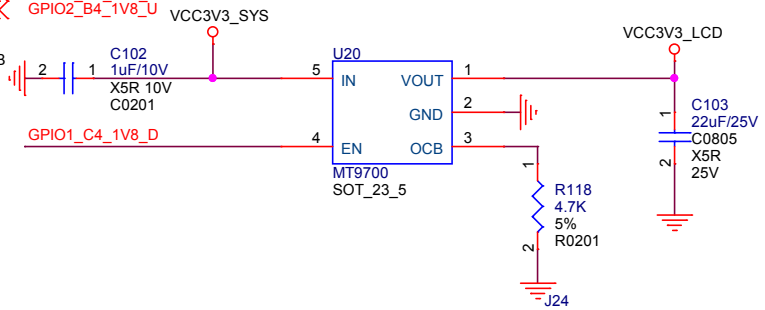
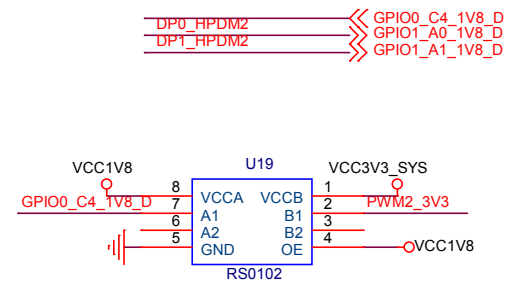
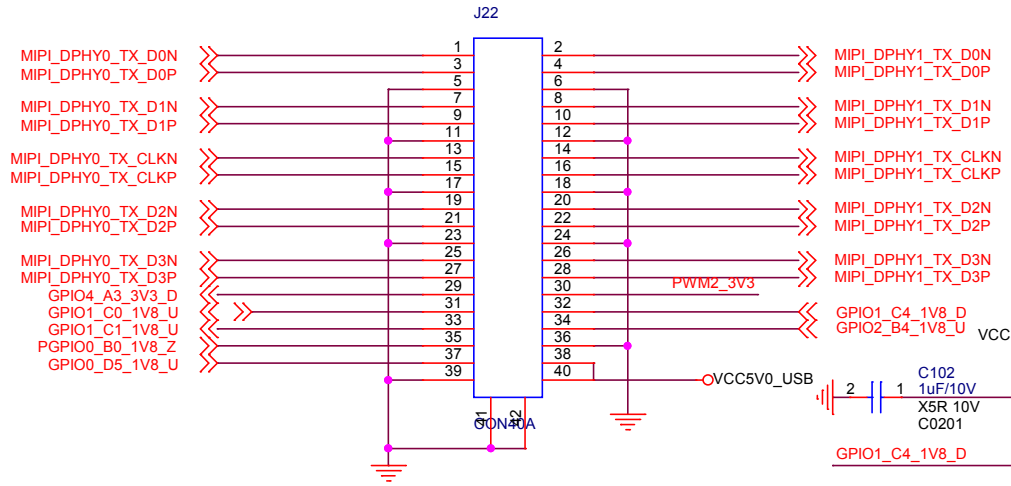


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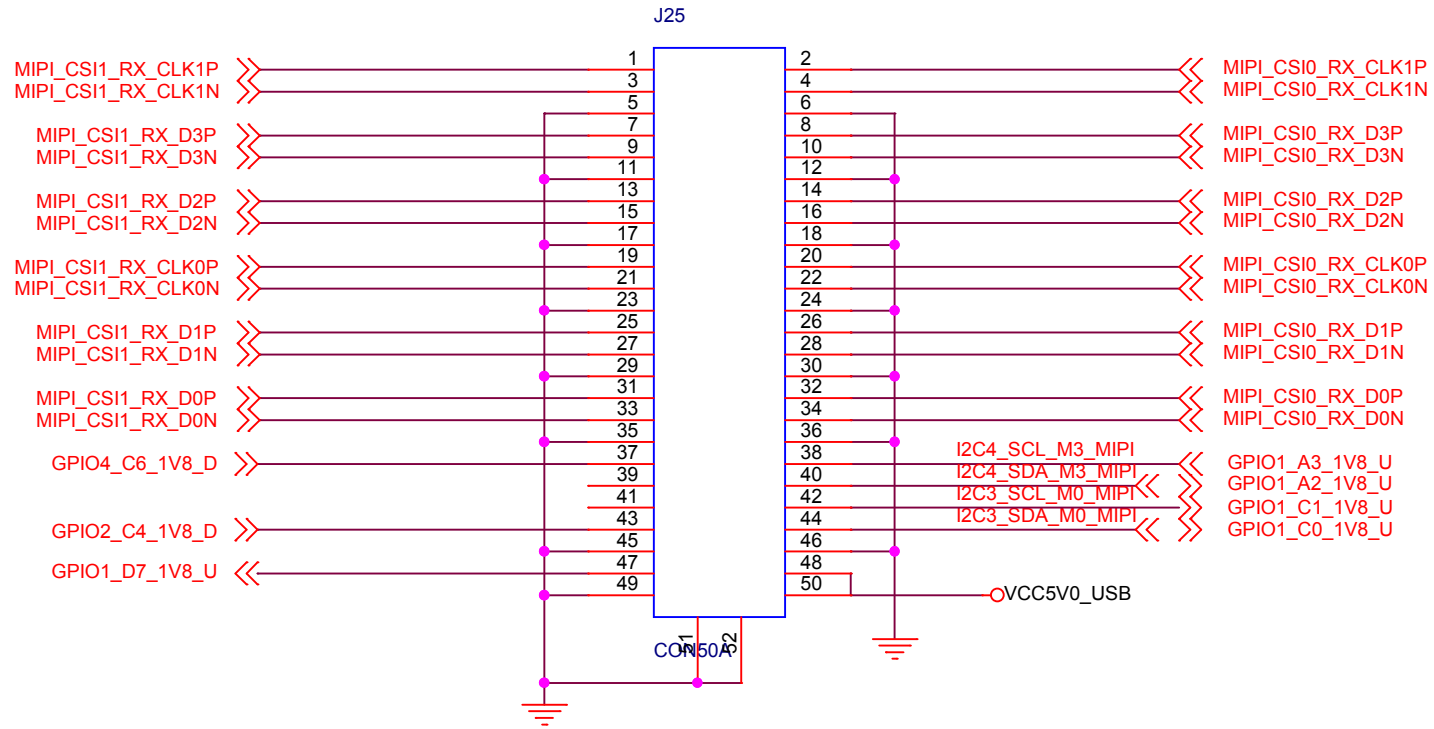
UART Debug



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